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Grace Alicea

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **RECEIVED**

In Re Application of:

Date: February 23, 2004 MAR 02 2004

Anthony CORREALE, Jr., et al.

Confirmation No. 9427

Serial No: 10/065,226

Group Art Unit: 2816

Filed: September 26, 2002

Examiner: Wells, Kenneth B.

For: METHOD AND CIRCUIT FOR OPTIMIZING POWER CONSUMPTION
IN A FLIP-FLOP

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Final Office Action dated August 21, 2003, please amend the above-identified application in the following manner:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this

Remarks/Arguments begin on page 5 of this paper.